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4	US 20030131214 A1	20030710	36	Masterless building block binding to partitions using identifiers and indicators	712/13
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8	US 20030037178 A1	20030220	82	System and method for emulating network communications between partitions of a computer system	719/319
9	US 20030016770 A1	20030123	195	Channel equalization system and method	375/346
10	US 20020103889 A1	20020801	39	Virtual storage layer approach for dynamically associating computer storage with processing hosts	709/223
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23	US 6133845 A	20001017	82	Furniture unit having a modular communication network	340/310.08
24	US 5942984 A	19990824	79	Communications network for identifying the location of articles relative to a floor plan	340/3.5

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25	US 5907285 A	19990525	81	Furniture unit having a modular communication network	340/3.5
26	US 5701502 A	19971223	152	Isolating a central processing unit from the operating system controlling said unit and its associated hardware for interaction of the unit with data handling apparatus alien to the operating system	709/201
27	US 5684469 A	19971104	83	Method of configuring a furniture utility distribution system	340/825.52
28	US 5530435 A	19960625	78	Utility distribution system for modular furniture and the like	340/825.52
29	US 5388215 A	19950207	157	Uncoupling a central processing unit from its associated hardware for interaction with data handling apparatus alien to the operating system controlling said unit and hardware	709/229
30	US 5369767 A	19941129	157	Servicing interrupt requests in a data processing system without using the services of an operating system	710/264
31	US 5369749 A	19941129	157	Method and apparatus for the direct transfer of information between application programs running on distinct processors without utilizing the services of one or both operating systems	718/104

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45	US 4894824 A	19900116	63	Control network for a rapid connection circuit switch	370/380
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49	US 4872159 A	19891003	62	Packet network architecture for providing rapid response time	370/352
50	US 4872158 A	19891003	62	Distributed control rapid connection circuit switch	370/380
51	US 4872157 A	19891003	63	Architecture and organization of a high performance metropolitan area telecommunications packet network	370/400
52	US 20030131330 A	20030710	17	Master less building block binding method for multiprocessor systems, involves sending physical port identifiers indicating logical location of building block, receiving and joining partition indicated by identifiers	